

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	4766	709/217.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2007/03/01 08:08
2	BRS	L2	377	709/217.ccls. and simula\$6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2007/03/01 08:08
3	BRS	L3	13	709/217.ccls. and simula\$6 and CAD	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2007/03/01 08:09
4	BRS	L4	30	707/10.ccls. and simula\$6 and CAD	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2007/03/01 08:10
5	BRS	L5	18	707/10.ccls. and simula\$6 and CAD and circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2007/03/01 08:12

Interference

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L7	34	(collab\$7 same circuit same design)	US-PGPUB	2007/03/01 09:14
2	BRS	L8	5014	(first same second same simulation)	US-PGPUB	2007/03/01 09:15
3	BRS	L9	167	(first same second same simulation same engine)	US-PGPUB	2007/03/01 09:15

TS